

### **IEEE 1588 Frequency and Time & phase profiles at ITU-T**

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- IEEE-1588™ Profile
- ITU-T G.8265.1 Frequency Profile
- ITU-T G.8275.1 Time and Phase Profile
- ITU-T G.8275.2 Time and Phase Profile with partial support from the network

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- IEEE-1588 defines profile as "The set of allowed Precision Time Protocol (PTP) features applicable to a device"
- "The purpose of a PTP profile is to allow organizations to specify specific selections of attribute values and optional features of PTP that, when using the same transport protocol, inter-work and achieve a performance that meets the requirements of a particular application."
- A PTP profile should define
  - Best master clock algorithm options
  - Configuration management options
  - Path delay mechanisms (peer delay or delay request-response)
  - The range and default values of all PTP configurable attributes and data set members
  - The transport mechanisms required, permitted, or prohibited
  - The node types required, permitted, or prohibited
  - The options required, permitted, or prohibited

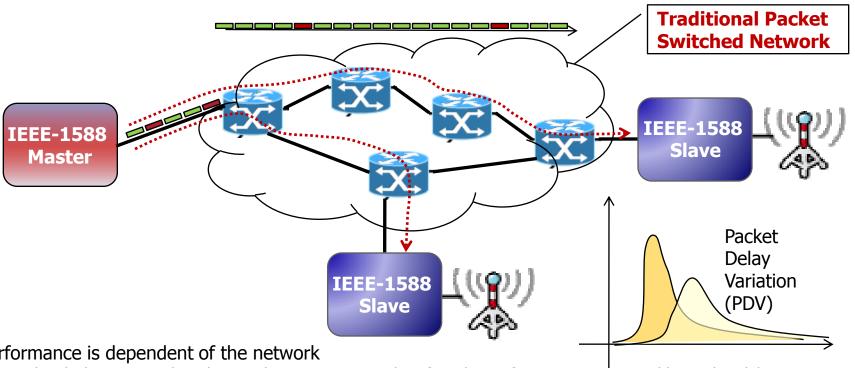
\* IEEE Std 1588-2008 IEEE Standard for a Precision Clock Synchronization Protocol, copyright 2008 IEEE. All right reserved.



### ITU-T FREQUENCY PROFILE



### **ITU-T G.8265.1 Frequency Profile IEEE-1588** without support from Network



- Performance is dependent of the network
  - The clock recovery algorithm is adaptive in nature, therefore the performance is impacted by packet delay variation in the network
- The quality of the clock delivered to the application depends on several factors
  - The quality of the oscillator at the slave, the packet delay variation of the network, the number of timing packets per second
- ITU-T consented several Recommendations for IEEE-1588 for Frequency Synchronization targeting wireless backhaul applications
  - G.8265 (Architecture and requirements for packet-based frequency delivery), G.8265.1 (Precision time protocol telecom profile for frequency synchronization), G.8263 (Timing Characteristics of Packet based Equipment Clocks (PEC)), G.8261.1 (Packet Delay Variation Network Limits applicable to Packet Based Methods), and G.8260 (definition of PDV metrics)



### **G.8265.1 - PTP Options and Configurable Attributes**

- One-way versus two-way mode
  - Both one-way and two-way modes are supported in the Frequency Profile
- Unicast versus Multicast mode
  - Only Unicast mode is allowed in the Frequency Profile
  - Unicast Message negotiation is used
- One-step versus two-step clock mode
  - Both one-step and two-step clocks are supported in the Frequency Profile

#### PTP mapping

- IEEE1588-2008 annex D Transport of PTP over User Datagram Protocol over Internet Protocol Version 4 is supported in the Frequency Profile
- IEEE1588-2008 annex E Transport of PTP over User Datagram Protocol over Internet Protocol Version 6 is supported in the Frequency Profile

#### PTP Message rates

- Sync /Follow-up min rate: 1 packet every 16 seconds, max rate: 128 packets per second
- Delay\_Request/Delay\_Response 1 packet every 16 seconds, max rate: 128 packets per second
- Announce min rate: 1 packet every 16 seconds, max rate: 8 packets per second, default: 1 packet every 2 seconds
- Signaling messages no rate is specified



### **Best Master Clock Algorithm (BMCA)**

- PTP allows the following options for the BMCA
  - The default BMCA specified in the IEEE-1588 standards
  - An alternate best master clock algorithm specified in a profile
- PTP specifies the requirements for an alternate best master clock algorithm
  - Provision must be made to provide the states required for operation of the PTP state machines and state decision
  - The alternate best master clock algorithm may be dynamic or static
  - A static algorithm will simply configure the recommended state values on the ports of the node on which it is running.
  - The state decision codes for use in updating the data sets must be provided as an output of the alternate best master clock algorithm



### **G.8265 - BMCA Telecom Requirements**

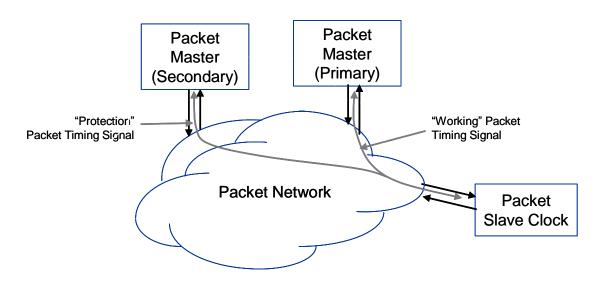


Figure 2/ITU-T G.8265.1: Packet network timing (frequency) protection

- Telecom architecture allows several masters delivering timing messages (e.g. Sync, Delay\_resp) to slaves at the same time
- Slaves should have the possibility to switch from one primary master to a backup master in case of failure
- Different slaves must be able to select different masters.
- The Master selection mechanism must be compatible with ITU-T Recommendation G.781 Synchronization Layer Functions (SDH and Synchronous Ethernet)



- For the alternate BMCA in G.8265.1, each master is isolated by a separated PTP domain that is done through the unicast communication
  - Grandmasters do not exchange Announce messages.
  - Masters are always active
  - Slaves are always slave-only clocks
- The Master selection process is based on the Quality Level (QL)-enabled mode per ITU-T Recommendation G.781
  - Quality Level (QL)
    - The Clock Class attribute in the Announce messages in PTP is used to carry the SSM QL value
    - Master with the highest Quality Level that is not in a failure condition will be selected
    - In case of Masters with similar QL, the Master with the highest Priority is selected.
  - Priority
    - Each master has a priority value that is locally maintained in the Telecom slave.
  - Packet Timing Signal Fail (PTSF)
    - PTSF-lossSync, PTSF-lossAnnounce, PTSF-unusable
- G.8265.1 introduces the concept of a Telecom Slave
  - Consists of multiple PTP slave-only clock instances.



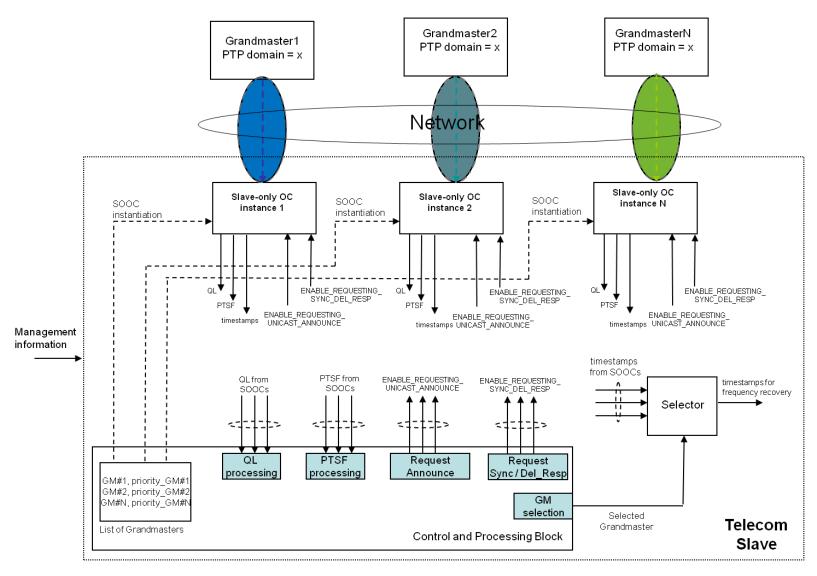


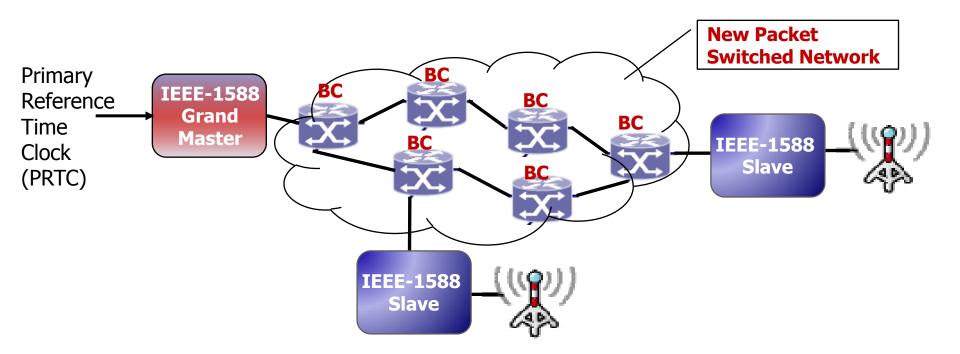
Figure 3/G.8265.1: Telecom Slave Model



## ITU-T TIME/PHASE PROFILES



## **G.8275.1- ITU-T Time/Phase Profile IEEE-1588 with support from Network**



- Boundary Clocks (BCs) and Transparent Clocks (TCs) can be used to overcome the performance issue
- The quality of the clock delivered to the application is dependent on the quality of the oscillator at each BC/TC and at the slave nodes
  - ITU decided that Synchronous Ethernet will be used in conjunction with BC
- The work on TC is just starting at ITU
- ITU-T is working on several recommendations (G.827x) to address time and phase applications



### **Time & Phase Profile - On-going work at ITU**

- IEEE-1588 Time & Phase Profile is part of a wider work at ITU targeting stringent requirements for phase synchronization
  - Targeting accuracy class levels 4, 5 and 6 of Table 1 of G.8271

Level of Accuracy	Range of requirements (Note 2)	Typical Applications
1	1 ms – 500 ms	Billing, Alarms
2	5 μs – 100 μs (Note 1)	IP Delay monitoring)
3	1.5 μs -5 μs	LTE TDD (large cell) Wimax-TDD (some configurations)
4	1 μs - 1.5 μs	UTRA-TDD, LTE-TDD (small cell)
5	x ns - 1 μs	Wimax-TDD (some configurations)
6	< x ns (x tbd, between 500 and 1000 ns).	Some LTE-A features (Note 3)

Table1/G.8271 – Time and Phase requirement classes

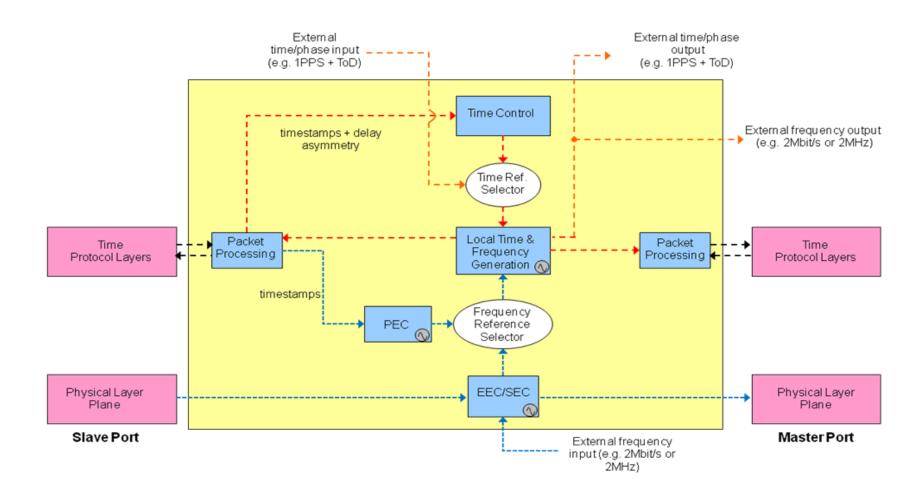


### **G.8275.1 PTP Options and Configurable Attributes**

- On-going work on the usage of the PTP Options and configurable attributes for phase and time profile
- Two types of Ordinary clocks: T-GM (Telecom Grand master, master clock only) and T-TSC (Telecom Slave clock, Slave-Only Ordinary Clock)
- Boundary clock will be used on the first profile
- One-step and two-step clocks are allowed
- PTP mappings
  - The default mapping for the Time/phase profile is agreed to be IEEE1588-2008 annex F -Transport of PTP over Ethernet
  - IEEE1588-2008 annex D Transport of PTP over User Datagram Protocol over Internet Protocol Version 4 is for further study
  - IEEE1588-2008 annex E Transport of PTP over User Datagram Protocol over Internet Protocol Version 6 is for further study
- Unicast versus Multicast mode
  - For the Ethernet mapping, non-forwardable multicast address 01-80-C2-00-00-0E must be used for all PTP messages
- PTP Message types
  - Sync message, Follow-up, Announce, Delay\_Request, and Delay\_Response



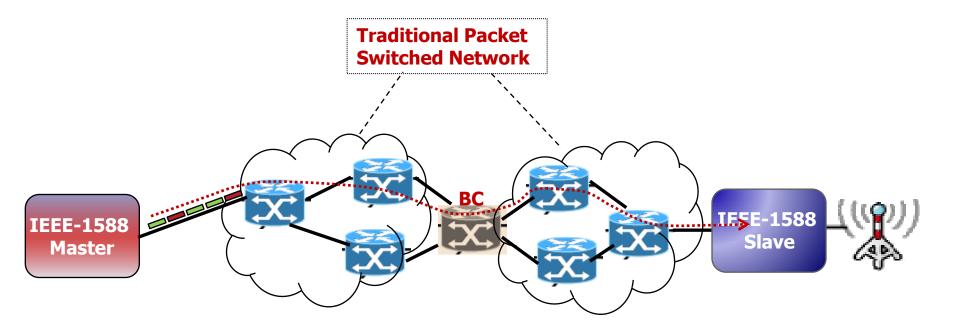
### **G.8273.2 - Telecom Boundary Clock Model**



**G.8273.2/Figure A. 1: Boundary Clock Model** 



# **G.8275.2- ITU-T Time/Phase Profile IEEE-1588 with partial support from Network**



- Development of a new PTP Profile for Time/Phase Distribution (G.8275.2)
- Architecture will need to be specified
- The performance aspects will need to be studied



### **THANK YOU!**